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10/511,868	10/16/2004	Kwok Hong Luk	CN02 0008 US	8915

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PHILIPS ELECTRONICS NORTH AMERICA CORPORATION  
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EXAMINER

MOON, SEOKYUN

ART UNIT	PAPER NUMBER
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2629

MAIL DATE	DELIVERY MODE
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06/05/2007

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

## Office Action Summary

**Application No.**

10/511,868

**Applicant(s)**

LUK, KWOK HONG

**Examiner**

Seokyun Moon

**Art Unit**

2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 21 March 2007.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1,2 and 6-11 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,2 and 6-11 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 21 March 2007 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
- 1) ☒ Certified copies of the priority documents have been received.
  - 2) ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - 3) ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)                     | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____                                      |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)          | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____  | 6) <input type="checkbox"/> Other: _____                          |

## DETAILED ACTION

### *Drawings*

1. Amended drawings were received on March 21, 2007 and these drawings are acceptable.  
Accordingly, the objection to the drawings has been withdrawn.

### *Response to Arguments*

2. The Applicants' arguments with respect to claims 1 and 6-11 have been considered but are moot in view of the new ground(s) of rejection.

### *Claim Rejections - 35 USC § 112*

3. The following is a quotation of the second paragraph of 35 U.S.C. 112:  
  
The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
4. **Claims 6 and 7** are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

As to **claim 6**, it is not clear as to whether the “*display device*” and the other claim limitations such as “*electronic apparatus*”, “*display panel*”, “*driving electronics*”, and “*interface*” common to both claims 1 and 2 are meant to be the same or different.

For further examination purpose, the claim will be interpreted as “*the display device according to claim 1, the display device having the display panel provided with the driving electronics and means for recognizing an identification code at the interface between the electronic apparatus and the display device*”, as best understood by the Examiner.

Appropriate correction/explanation is required.

As to **claim 7**, it is not clear as to whether the claim limitation, “*display device*”, common to both claims 1, 6, and 7 are meant to be the same or different.

For further examination purpose, the claim limitation will be interpreted as “*the display device*” instead of “*a display device*”, as best understood by the Examiner.

Appropriate correction/explanation is required.

***Claim Rejections - 35 USC § 102***

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

6. **Claims 1, 2, 6, and 8-11** are rejected under 35 U.S.C. 102(b) as being anticipated by Nitta et al. (US 2001/0004257, herein after “*Nitta*”).

As to **claim 1**, Nitta teaches an electronic apparatus [fig. 2] suitable for displaying information via a display device (a combination of “*display screen*” and “*ASIC 21*”) [par. (0029) lines 1-3], the display device having a display panel (“*display screen*”) provided with driving electronics (“*ASIC 21*”), the electronic apparatus comprising a controller (“*multiplexer 31*”) [fig. 2] for selecting at least one application (data processing of “*EDID for the VGA interface*” or “*EDID for the DVI-I interface*”) for the display device [par. (0031), emphasis on lines 4-5] and further comprising memory means (“*memories 23 or 25*”) for storing at least display parameters (“*EDID for the VGA interface*” or “*EDID for the DVI-I interface*”) related to the application and means (“*DDC clock line 27*” and “*DDC data line 29*”) for providing the display parameters to an interface between the electronic apparatus and the display device [fig. 2], the display parameters belonging to a group consisting of: a number of lines to be displayed (par. (0011) lines 4-8, note that resolution is referred to as a number of pixel columns by the number of pixel

rows, thus the number of lines to be displayed is equivalent to the number of pixel rows), a number of columns to be displayed (par. (0011) lines 4-8, note that resolution is referred to as a number of pixel columns by the number of pixel rows, thus the number of columns to be displayed is equivalent to the number of pixel columns), parameters ("*frequency of vertical scan signals*") related to driving transistors of the display device, and power saving parameters for the display device [par. (0011) lines 4-8 and par. (0052)].

As to **claim 2**, Nitta teaches the electronic apparatus [fig 2] further comprising memory means ("*memories 23 or 25*") [fig. 2] for storing parameters ("*EDID for the VGA interface*" or "*EDID for the DVI-I interface*") related to the selection of driving transistors [par. (0011) lines 4-8].

As to **claim 6**, Nitta teaches the display device having the display panel ("*display screen*") provided with the driving electronics ("*ASIC 21*") [par. (0029) lines 1-3] and means ("*9*") [fig. 2] for recognizing an identification code ("*EDID*") at an interface between the electronic apparatus and the display device.

As to **claim 8**, Nitta teaches the display parameters ("*frequency of vertical scan signals*" and "*frame rate*") [par. (0011) lines 4-8] including at least one of a gate select width, a gate enable width ("*frame rate*" and "*frequency of vertical scan signals*" indicates how long it takes for each of the gates of the transistors included in the display panel to be enabled again after each of the gates of the transistors are enabled once), and a power saving pulse width.

As to **claim 9**, Nitta teaches a method of an electronic apparatus [fig. 2] controlling a display device (a combination of "*display screen*" and "*ASIC 21*") for at least one application (data processing of "*EDID for the VGA interface*" or "*EDID for the DVI-I interface*"), the method comprising:

programming into a memory ("*memories 23 or 25*") of the electronic apparatus display parameters ("*EDID for the VGA interface*" or "*EDID for the DVI-I interface*") related to the application, the display parameters including at least one selected from a group consisting of: number of lines to be

displayed (par. (0011) lines 4-8, note that resolution is referred to as a number of pixel columns by the number of pixel rows, thus the number of lines to be displayed is equivalent to the number of pixel rows), a number of columns to be displayed (par. (0011) lines 4-8, note that resolution is referred to as a number of pixel columns by the number of pixel rows, thus the number of columns to be displayed is equivalent to the number of pixel columns), parameters ("*frequency of vertical scan signals*") related to driving transistors of the display device, and power saving parameters for the display device [par. (0011) lines 4-8 and par. (0052)]; and

providing the display parameters ("*EDID for the VGA interface*" or "*EDID for the DVI-I interface*") from the electronic apparatus to the display device.

As to **claim 10**, Nitta teaches the method comprising storing the display parameters ("*EDID for the VGA interface*" or "*EDID for the DVI-I interface*") in a memory ("*memories 23 or 25*") [fig. 2] of the display device.

As to **claim 11**, all of the claim limitations have already been discussed with respect to the rejection of claim 8.

### ***Claim Rejections - 35 USC § 103***

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

8. **Claim 7** is rejected under 35 U.S.C. 103(a) as being unpatentable over Nitta.

Nitta teaches the display device comprising the driving electronics ("*ASIC 21*") [fig. 2] processing a sequence of parameters controlling the panel received via the interface from the electronic apparatus [par. (0029)].

Nitta does not expressly disclose the driving electronics comprising storage means for storing the sequence of the parameters.

However, Examiner takes official notice that it is well known in the art to include a memory in an application-specific integrated circuit (herein after, "*ASIC*") when ASIC is used as driving electronics of a liquid crystal display.

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to include a storage means in the ASIC of the display device of Nitta, in order to reduce the space required to implement driving electronics, by implementing storage means in ASIC rather than providing ASIC and storage means separately.

#### ***Conclusion***

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Seokyun Moon whose telephone number is (571) 272-5552. The examiner can normally be reached on Mon - Fri (8:30 a.m. - 5:00 p.m.).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on (572) 272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

May 24, 2007

- s.m.-

  
SUMATI LEFKOWITZ  
SUPERVISORY PATENT EXAMINER